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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10010890	FILING DATE 12/36/2001	CLASS 431	SUBCLASS 431	GAU 2812	EXAMINER
**APPLICANTS: Sakamoto Noriaki; Kobayashi Yoshiyuki; Sakamoto Junji; Okada Yukio; Igarashi Yusuke; Maehara Eiju; Takahashi Kouji;					
**CONTINUING DATA VERIFIED:					
** FOREIGN APPLICATIONS VERIFIED: JAPAN P 2001-083558 03/22/2001					
PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>			
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no Verified and Acknowledged Examiners's initials				ATTORNEY DOCKET NO 10417-107001	
TITLE : Method for manufacturing circuit device					

U.S. DEPT. OF COMM./PAT. & TM-PTO-436L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED			
		Assistant Examiner	Total Claims		
			Print Claim for O.G.		
ISSUE FEE		Primary Examiner	DRAWING		
Amount Due	Date Paid		Sheets Drwg.	Figs. Drwg.	Print Fig.
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE	Application Examiner		
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